



LIST OF GRADUATE THESES
as of March 2021
Diploma in Power Electronics

- Abas, C. (2007). *A study on an interleaved zero voltage switching (ZVS) multi-resonant (MR) single-ended primary inductance converter(SEPIC)* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Aco, J. F., Lacson, M. L. (2014). *Design of 100W 24V single output self-starting quasi-resonant flyback converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Ajo, V. R. R. [et al]. (2009). *200kHz 100W parallel tapped-inductor buck ZVS PWM converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Albais, K. P., Arciaga, J. G., Collamar, R. (2012). *Flyback converter training module using AS38442 IC* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Alvarez, M. G. and Cruz, B. C. (2006). *Design of a current control mode two FET forward interleaved converter designs for medium-power single-output 300-watt switch-mode power supply* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Andres, C. B., Camba, A. M. P. and Redula, R. G. E. (2018). *Design of trimmable output current-mode control LLC series resonant converter with synchronous rectification* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- Angelito, A. N. B. [et.al.]. (2007). *A Variable frequency coupled-inductor cuk converter using half-wave multi-resonant zero voltage-switching* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Apacible, J. A. and Gonzales, F. C. D.V. (2015). *Design and Implementation of a 300W Analog Controlled Average Current-Mode Fixed-Frequency Boost Converter Power Factor Correction Block* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Aurora, R. G. and Laudencia, L. A. A. (2014). *Design of 300W Interleaved Two-field effect Transistor Forward (ITTF) converter with Synchronous Rectification (SR)* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Bugarin, B., Catly, J. A., Faurillo, G. D. D. and Quinto, J. R. (2006). *Design of a half-bridge zero-current switching (ZCS) quasi-resonant converter (QRC)* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.



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- Caguimbal, M. V. [et al.]. (2009). *300W zero voltage transition power factor correction circuit using critically discontinuous controller* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Calderon, L. R. (2016). *50 Watts High Efficiency Low No-Load Input Power Fixed Frequency Discontinuous Conduction Mode Flyback Converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Caragay, F. C. [et al.]. (2005). *Design of a 144W stepdown ZVS half-bridge LLC resonant converter using TEA1610P controller* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Castillo, C. T. [et al.]. (2009). *A 120W non-isolated, non-inverting half-wave zero voltage switching quasi-resonant buck-boost converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Castro, F. Y., Heredia, A. P. D. (2013). *Digital Controller Board for 200W Power Factor Correction Circuit using dsPic 33FJ16GS504* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Checa, M. J., Cumabig, J. C., Payadkeo, F. and Viernes, J. R. (2006). *An Efficient 12-volt current-mode lamp ballast* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Cifra, F. V. [et al.]. (2007). *Development of a 300W 3-phase interleaved active clamp zero voltage switching forward converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Cleofe, M. D. and Dela Cruz, R. R. (2014). *Design of 300W DC/DC block LLC resonant converter using analog controller NCP1396A* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Cordel, M. O., De Quiroz, K. L. and Roldan, R. J. B. (2007). *Design of a high frequency DC-DC flyback converter using multi-resonant zero-voltage switching* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Da Jose, L. et al.. (2008). *Digital control of a two switch forward converter using the TMS320F2808 digital signal processor* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.



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- Dangalan, E. J. M. (2015). *Design of a Wide Range input, 18VDC, 100W Active Clamp Flyback Converter With Zero Voltage Switching* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- De Castro, P. G. (2013). *A Design of a 200-Watt LLC Resonant Half-Bridge Converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- De la Pena, M. C. (2013). *Design of Universal Mains Input 12V/50W Output Quasi-Restaurant Flyback Converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- De los Santos, J. J., Rivera, C. Z. C., Villanueva, E. J. G. (2018). *Design of a 350W two-switch forward converter with synchronous rectification and droop current sharing* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- De Villa, C. J. M., Phodaca, C. J. and R., Job, S. L. (2008). *500W zero voltage switching asymmetrical half-bridge converter with unbalanced secondary winding* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Dizon, C. P., Francisco, A. J. S., Quismundo, J. P. B. (2018). *300W Analog controlled continuous conduction mode boost converter power factor correction block with protection circuits* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- Domingo, E., Mendoza, K. J. M. (2017). *Design of 350W Two-Switch Forward Converter with Master-Slave Active Current Sharing Controller* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- Dy, A. C. P., Magadia, A. D. (2016). *200W Voltage-Mode Controlled Two-FET Forward Converter with Constant-Current Operation* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Escala, H. C. [et al]. (2008). *200-watt forward converter with a 100-watt output implementing secondary side post regulator (SSPR)* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Gayondato, J. M. and Nuevo, D. E. (2014). *Design and implementation of a 300W analog controlled power factor correction block in a critically discontinuous conduction module* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.



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- Guanlao, M. M. C. (2016). *350W Continuous Conduction Mode Two-Phase Interleaved Boost Converter Power Factor Correction Block* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Guzman, J. M. , Tan, J. A. O. (2017). *Design of a Two-Switch Forward Converter with Master-Slave Active Current Controller using a Digital Signal Processor* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- Ibanez, Q. et al. (2015). *Design of 200W average current mode controlled two-field effect transistor forward converter with synchronous rectification* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Javal, M. D. T., Yumol, S. K. M. (2017). *Design of a 225 W series LLC Resonant Converter with Charge Current Mode Control* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- Lazo, K. R. (2015). *Design and Implementaion of A 350W Voltage-Mode Control ZVS Full-Bridge Forward Converter with Center-Tapped Secondary and Synchronous Rectification* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Lime, J. K. A. (2013). *Power Factor Corrector Utilizing Boost Converter in CRM Operation* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Manadong, M. G., Nueda, R, I. S. (2017). *Design of 60W Active-Clamp Flyback Converter with Synchronus Rectification* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.
- Montalbo, P. F. B. [et al]. (2009). *A 100W zero voltage transition pulse width modulation flyback converter with synchronous rectification* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Pontanares, S. III E. (2016). *12.5W Digital Controlled Voltage-Mode Synchronous Buck Converter* [Master's Thesis, DPE]. School of Graduate Studies, Mapua Institute of Technology.
- Tuboro, A. Z. A., Estepa, T. T. (2017). *Design of a 300 W Boundary Conduction Mode Interleaved Boost Coverter PFC Pre-Regulation* [Master's Thesis, DPE]. School of Graduate Studies, Mapua University.